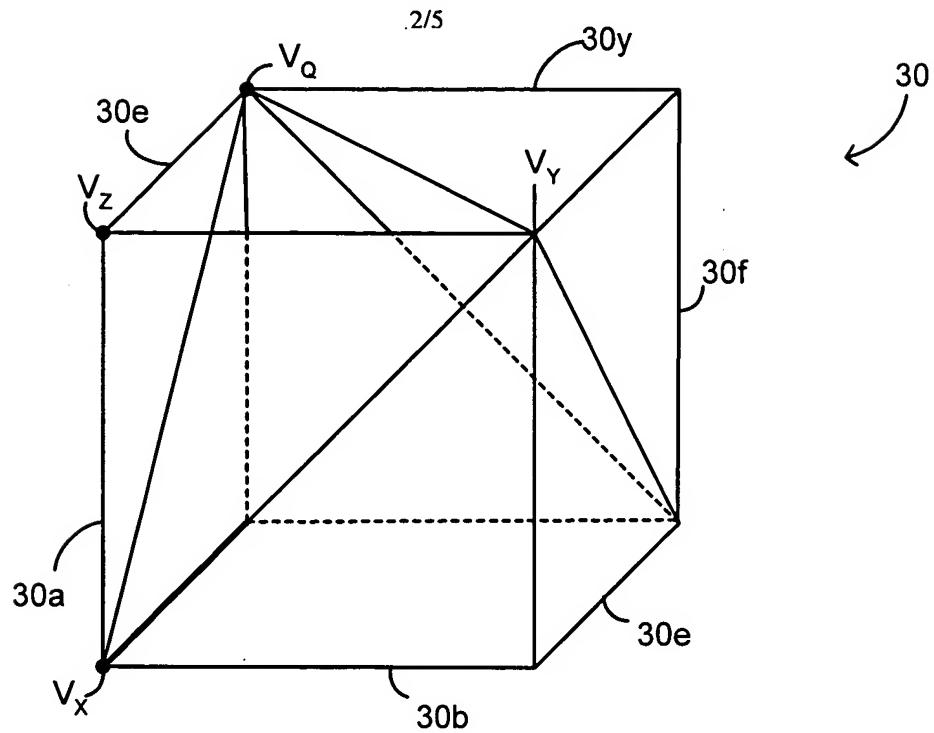
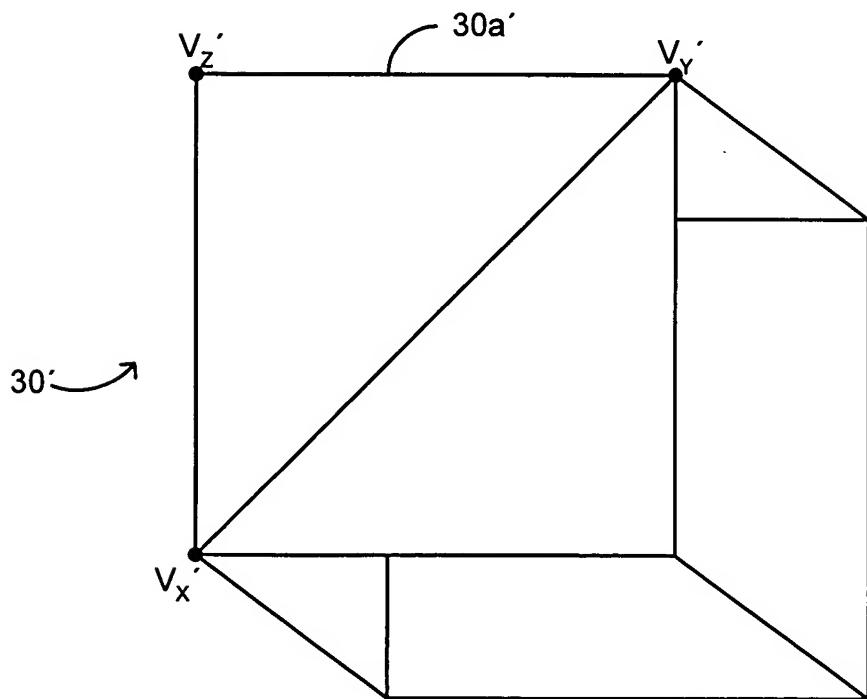


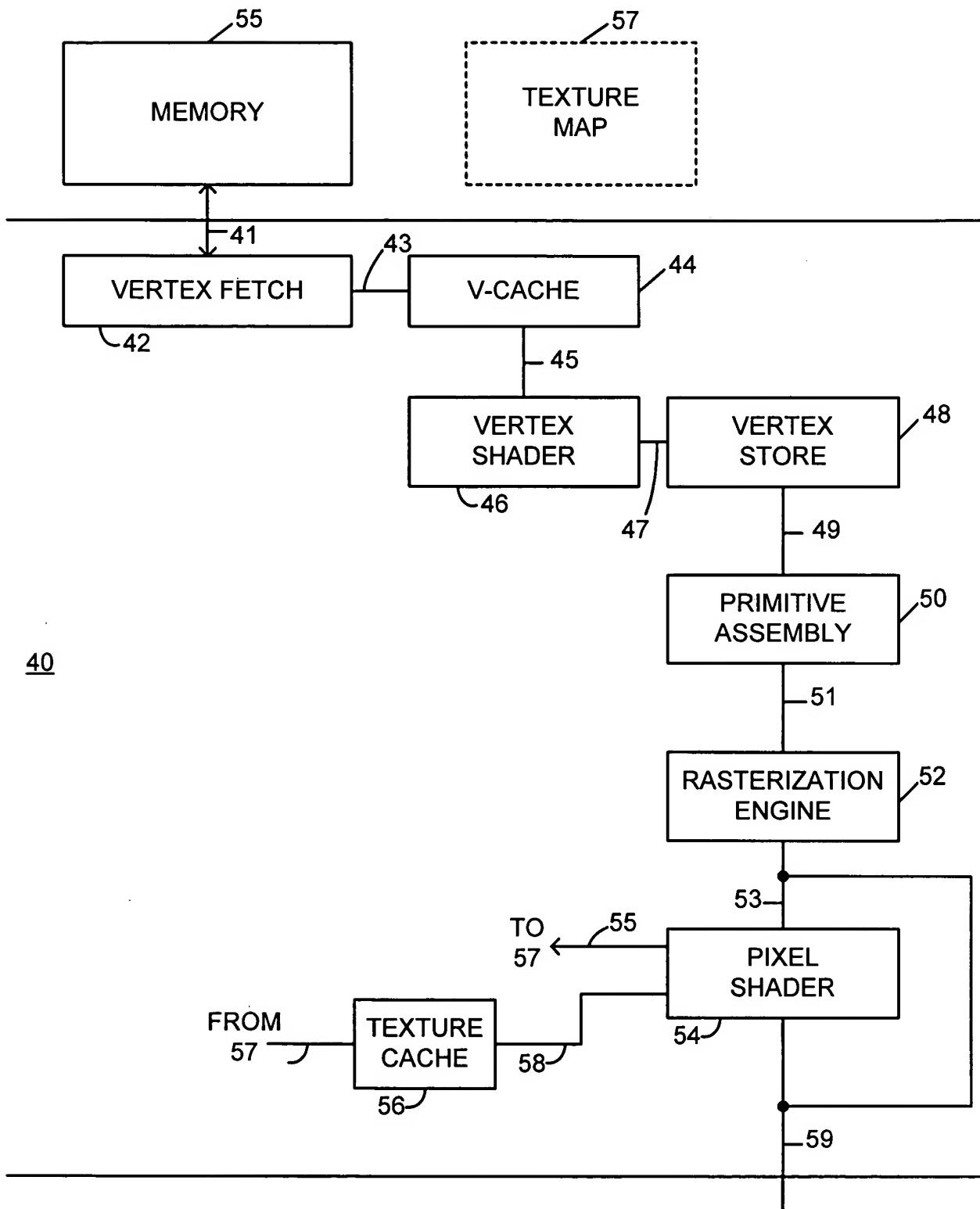
**FIG. 1**  
**(PRIOR ART)**



**FIG. 2A  
(PRIOR ART)**



**FIG. 2B  
(PRIOR ART)**



**FIG. 3**  
**(PRIOR ART)**

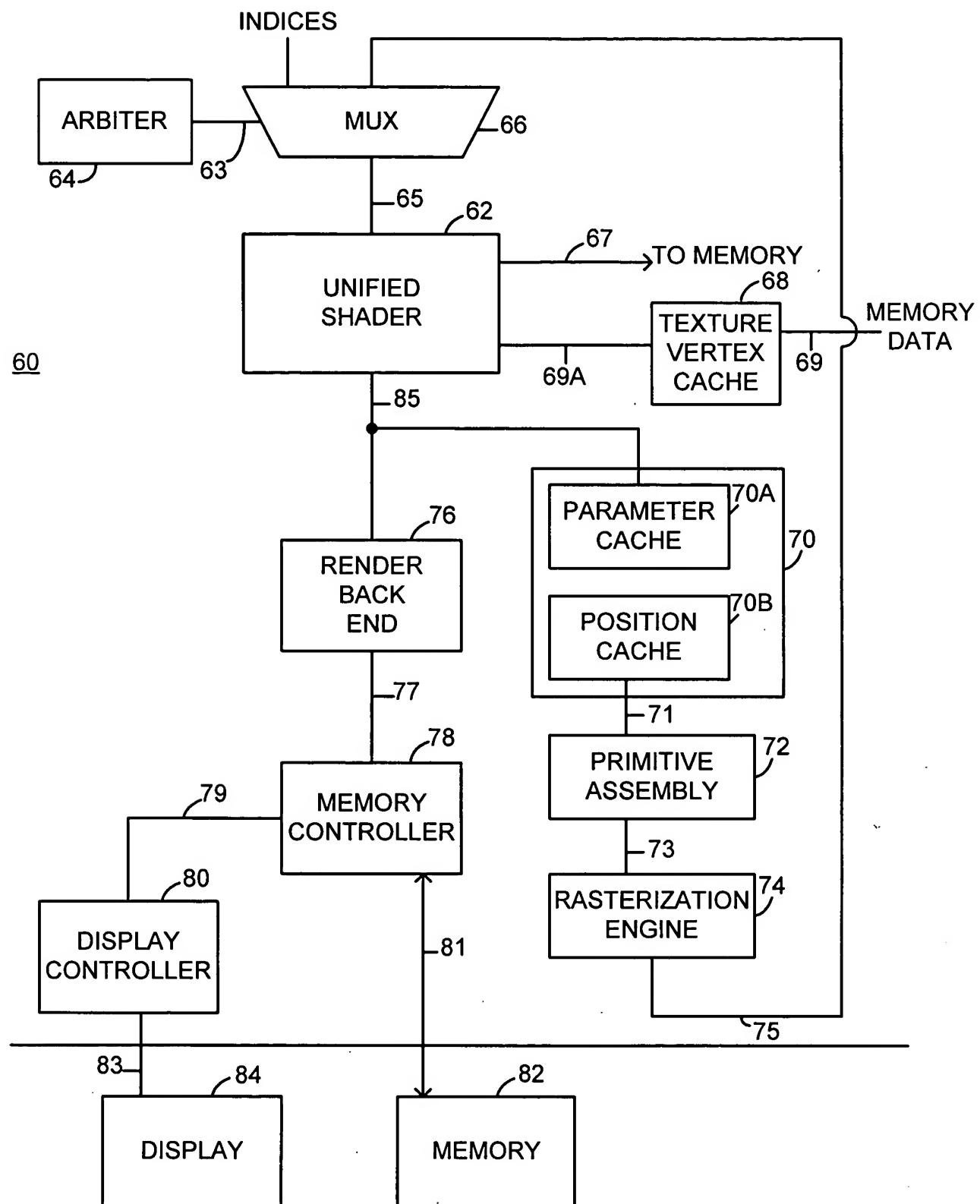
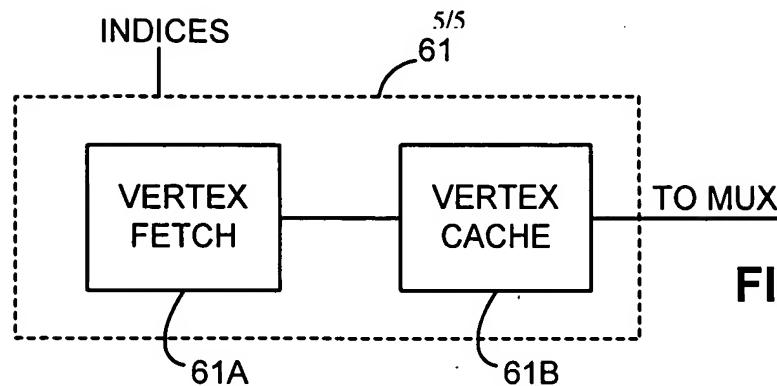
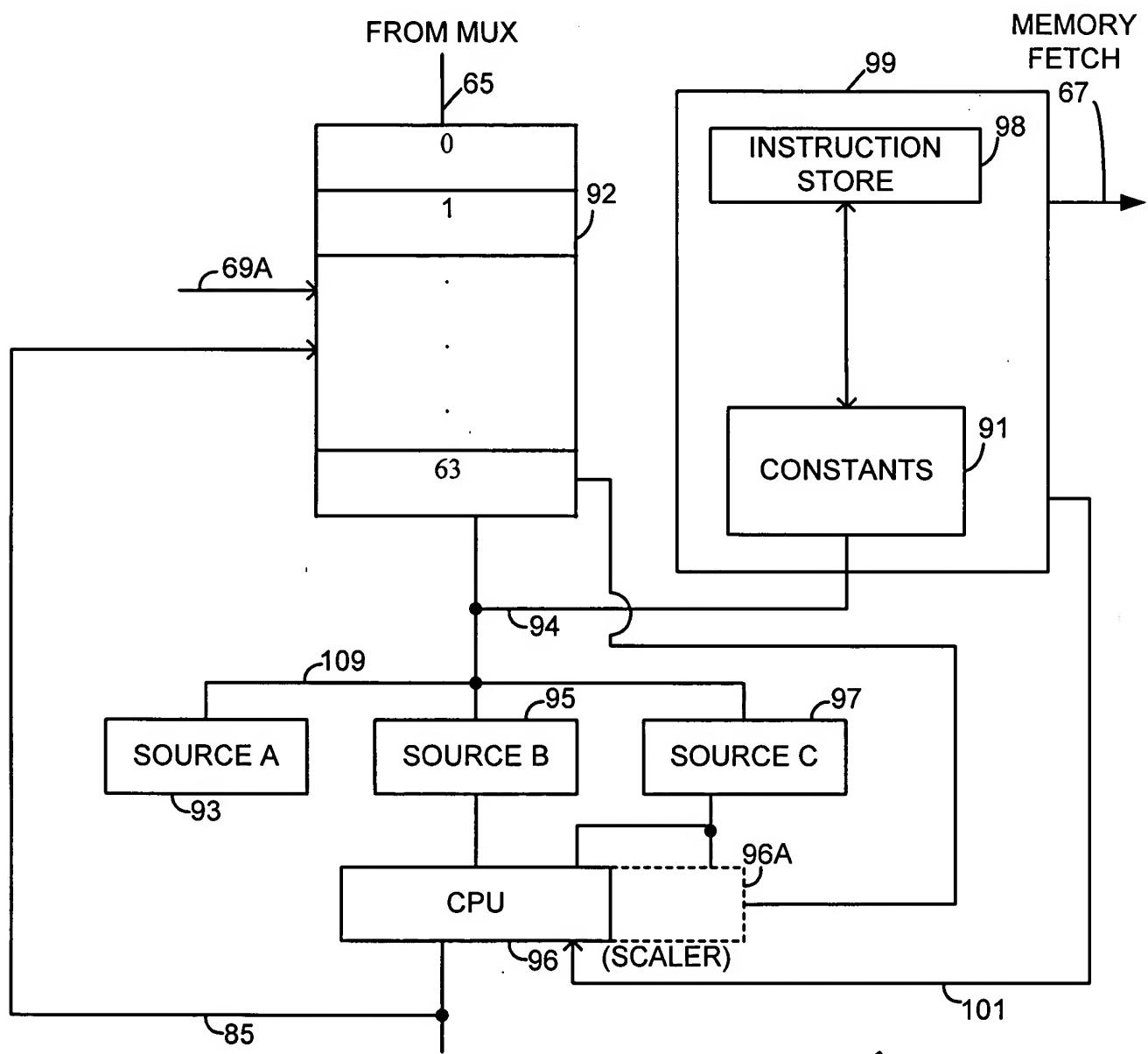


FIG. 4A

"A Graphics Processing Architecture Employing A Unified Shader"  
Inventors: Morein et al.  
Docket No. 0100.02.0001



**FIG. 4B**



**FIG. 5**